Trigger Logic Using Memory Bits for SeaQuest E-906

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ABSTRACT

The SeaQuest E-906 at Fermi National Accelerator Laboratory is a fixed target Drell-Yan process experiment to measure the $\bar{u}\bar{d}$ asymmetry in proton quark sea using the proton beam from the Main Injector at 120 GeV. The SeaQuest trigger system consists of four hodoscope stations guided by coincidence logic to select candidate Drell-Yan dimuon tracks. The trigger electronics involves a CAEN VME 1495, which is an FPGA implementation board. Two basic ways of implementing the trigger logic using FPGA are based either on the gate elements or on the memory bits. The feasibility of using digitized hodoscope signal output wires themselves to address the memory bits, in order to implement the SeaQuest E-906 trigger logic in the memory, was investigated.